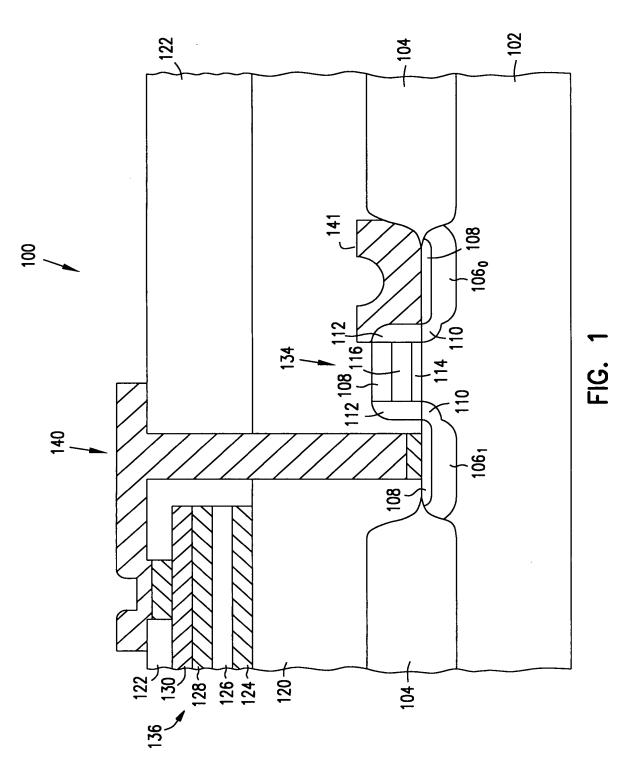
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INVENTOR'S NAME: VISHNU K. AGARWAL
SERIAL NO.: 10/615,891 DOCKET NO.: 303.628US2

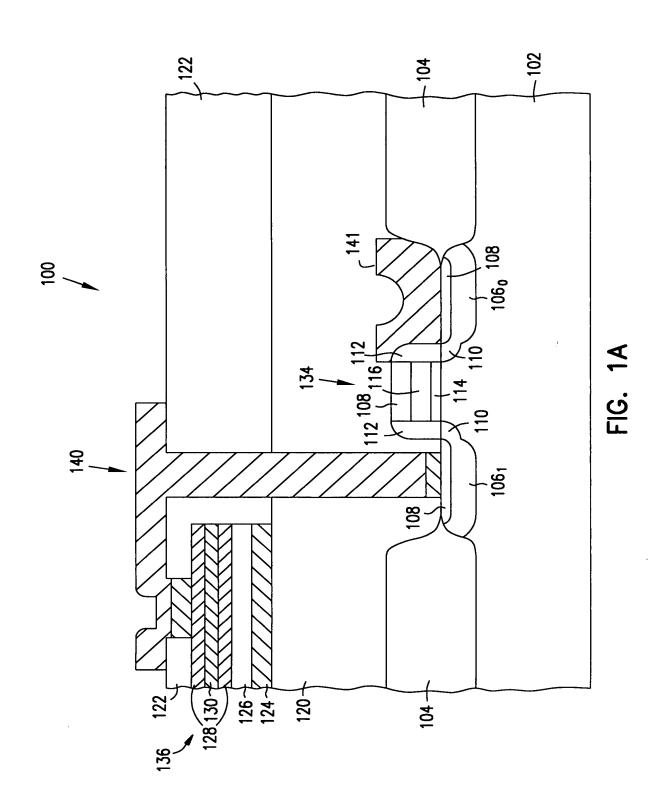
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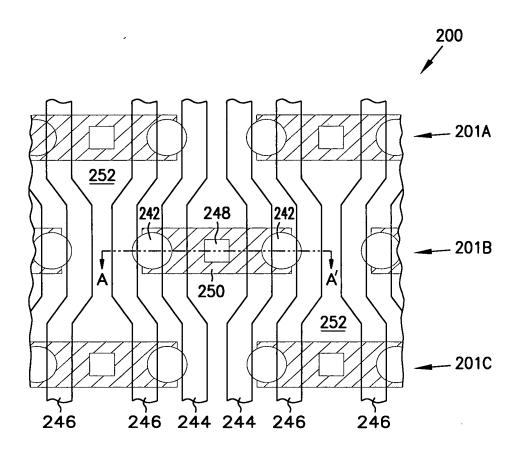
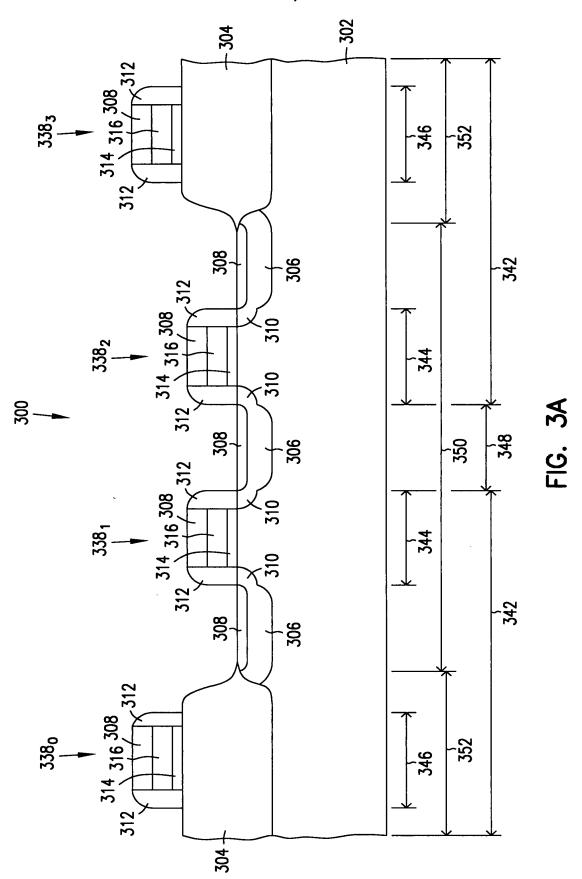


FIG. 2

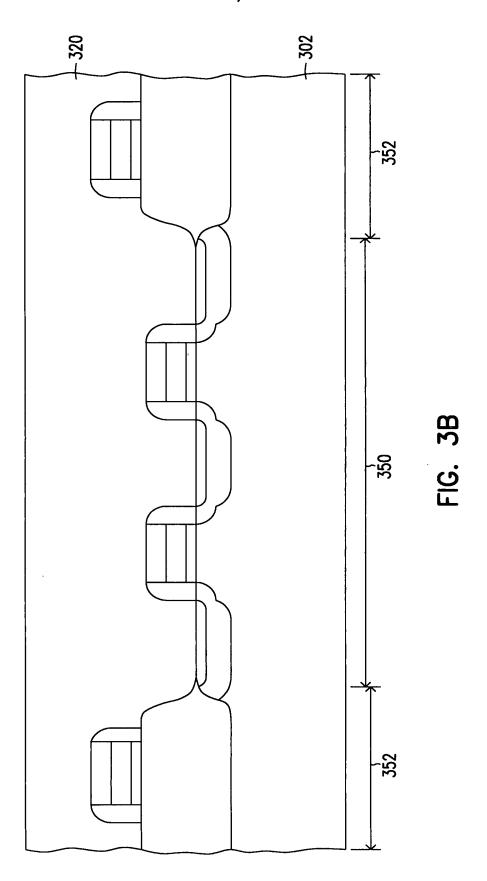
INVENTOR'S NAME: VISHNU K. AGARWAL

SERIAL NO.: 10/615,891 DOCKET NO.: 303.628US2 REPLACEMENT SHEET



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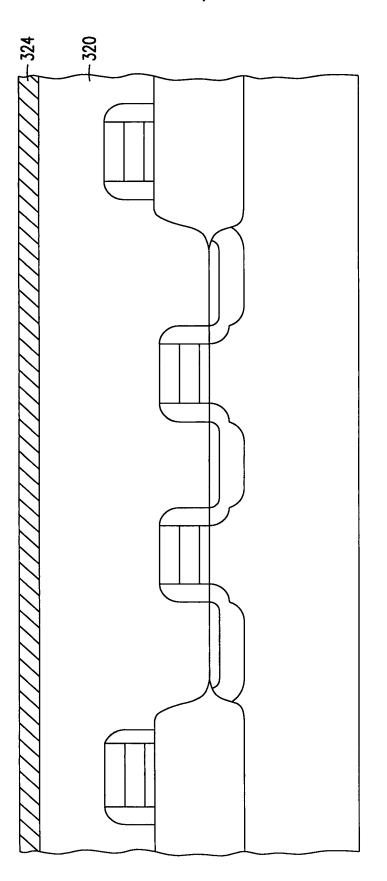


FIG. 3C

INVENTOR'S NAME: VISHNU K. AGARWAL

SERIAL NO.: 10/615,891 DOCKET NO.: 303.628US2

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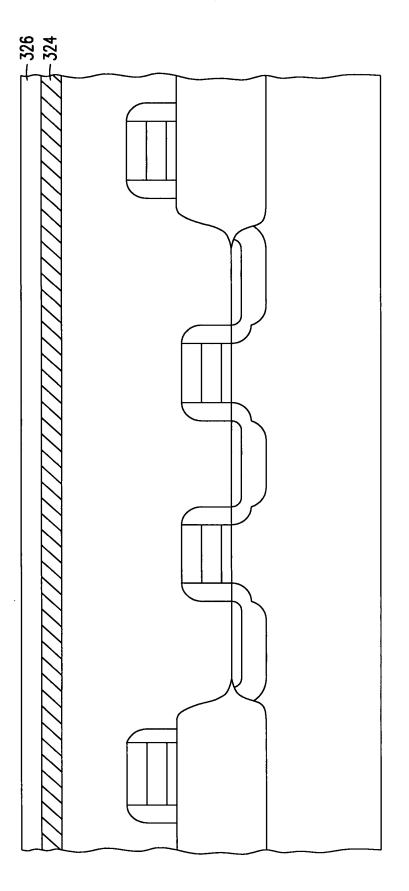
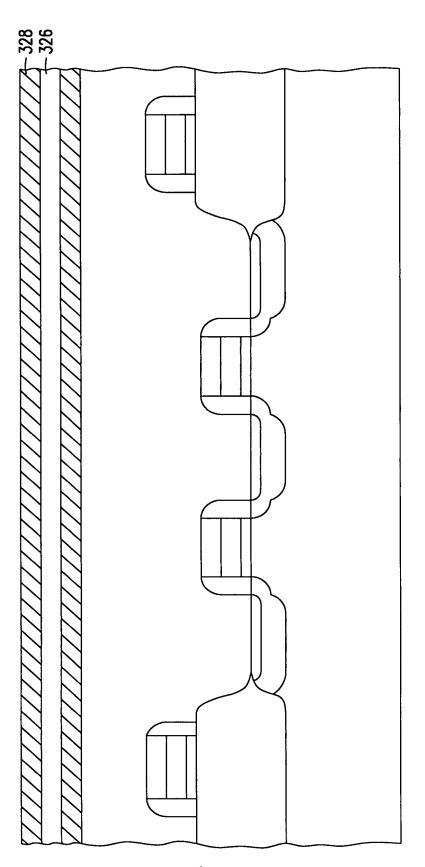


FIG. 3D

INVENTOR'S NAME: VISHNU K. AGARWAL SERIAL NO.: 10/615,891 DOCKET NO.: 303.628US2

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SERIAL NO.: 10/615,891 DOCKET NO.: 303.628US2 REPLACEMENT SHEET

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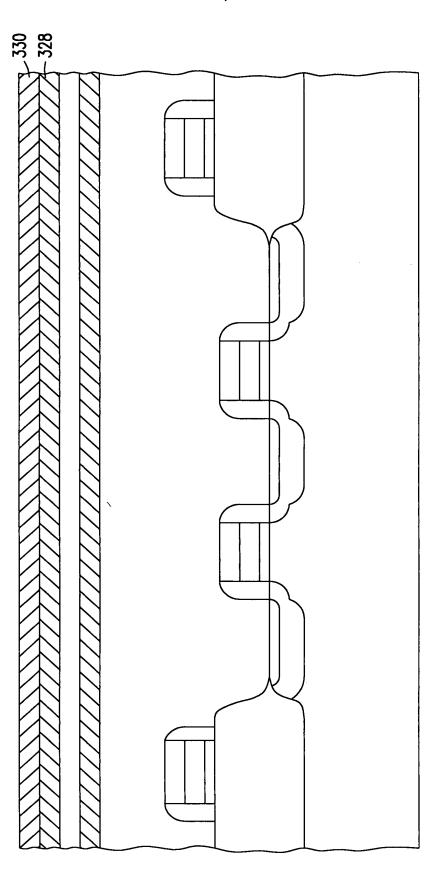
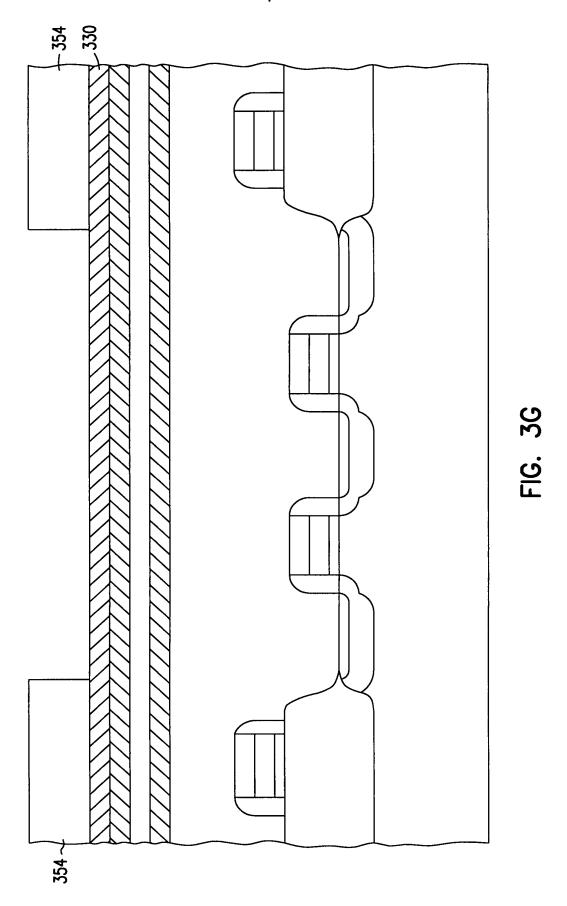
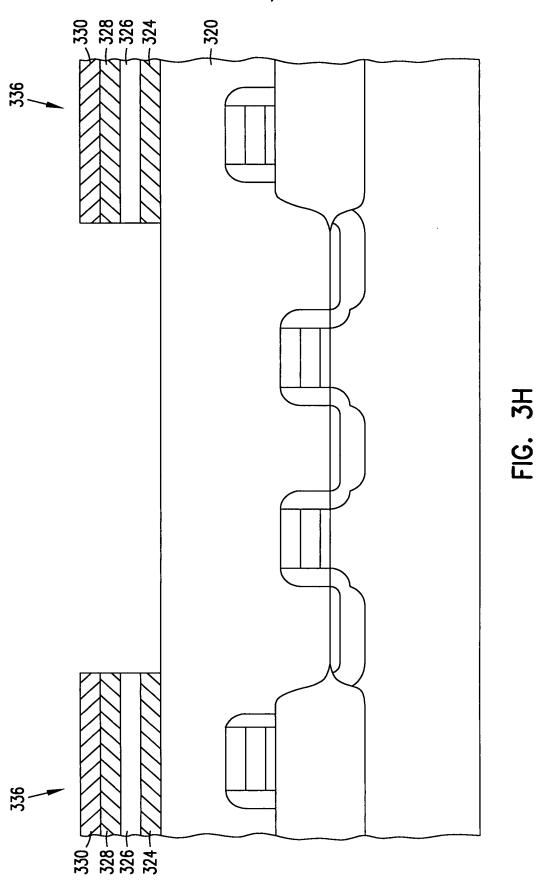


FIG. 3F

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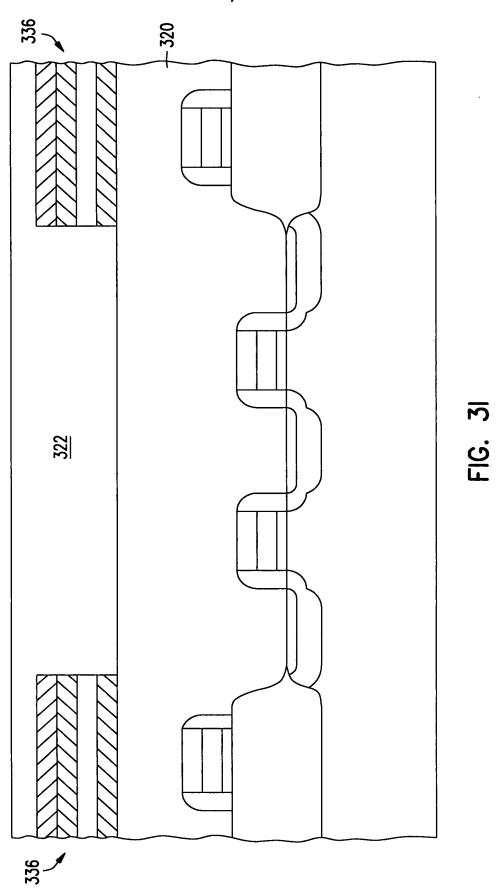


INVENTOR'S NAME: VISHNU K. AGARWAL SERIAL NO.: 10/615,891 DOCKET NO.: 303.628US2 REPLACEMENT SHEET

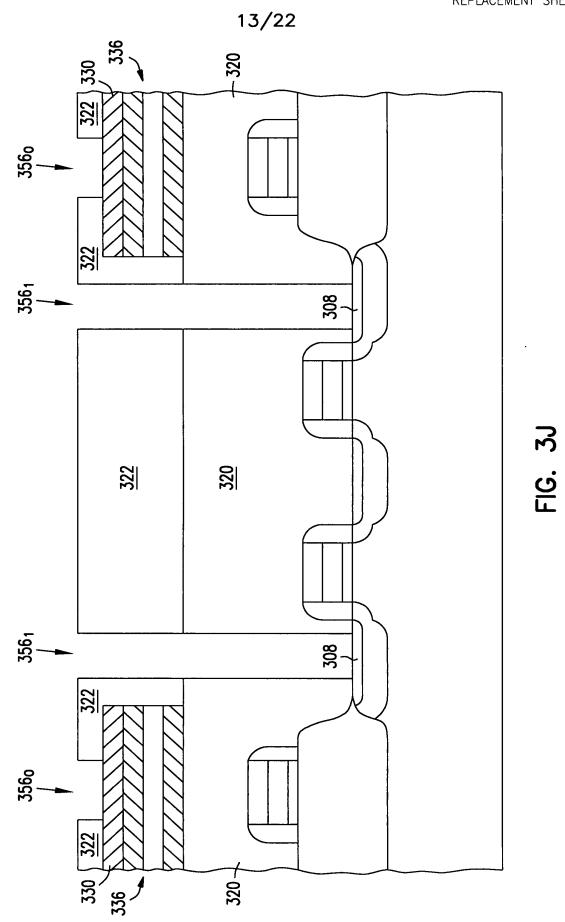


INVENTOR'S NAME: VISHNU K. AGARWAL

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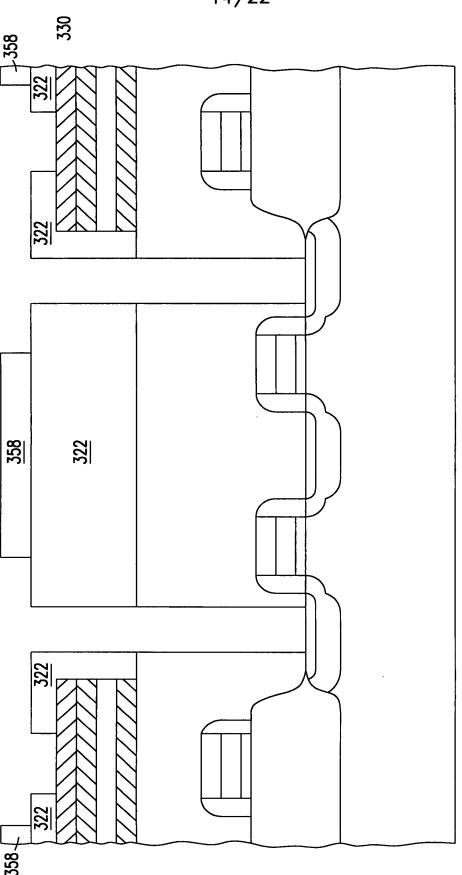
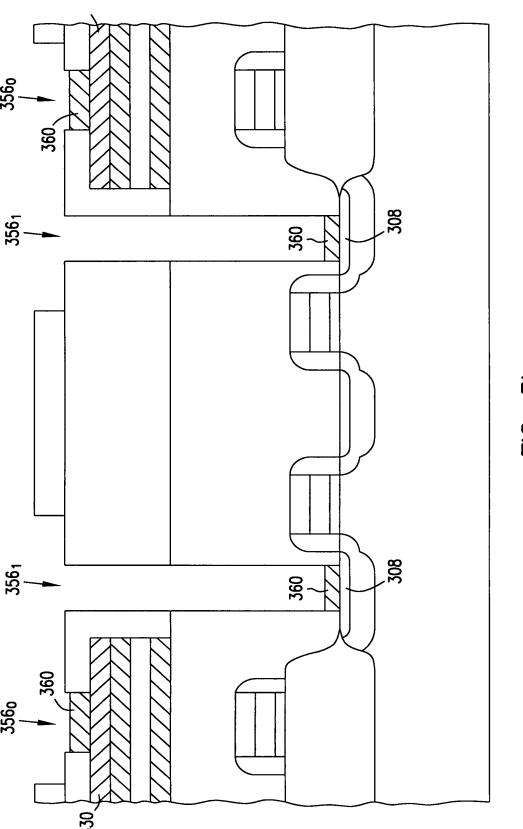


FIG. SK

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SERIAL NO.: 10/615,891 DOCKET NO.: 303.628US2

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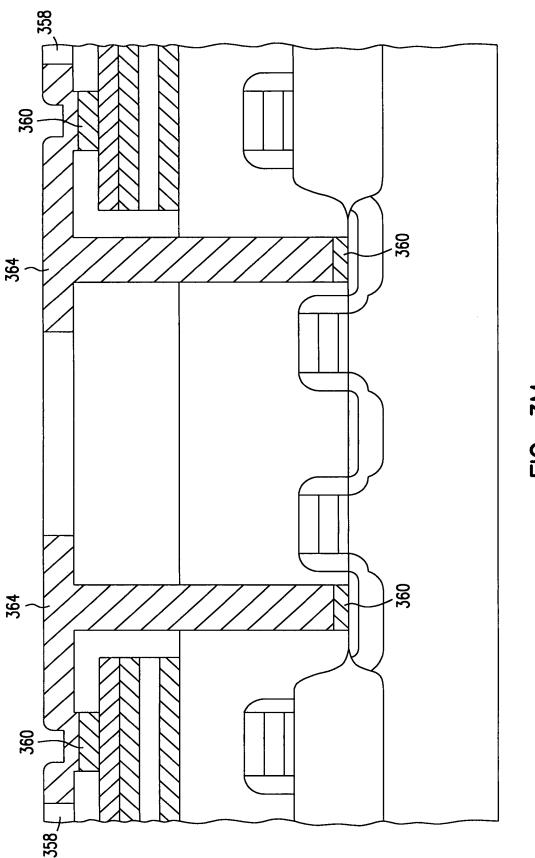


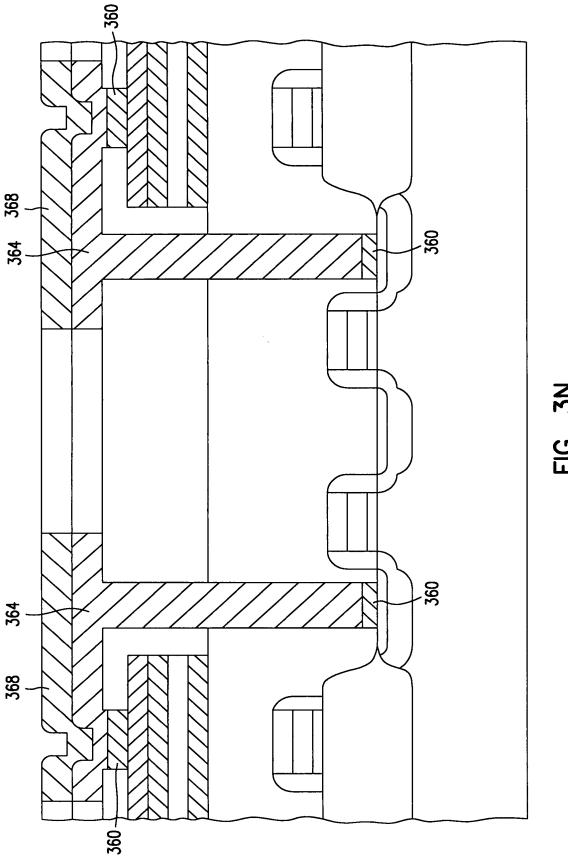
FIG. 3M

TITLE: STRUCTURES AND METHODS FOR IMPROVED CAPACITOR CELLS IN INTEGRATED CIRCUITS INVENTOR'S NAME: VISHNU K. AGARWAL

SERIAL NO.: 10/615,891 DOCKET NO.: 303.628US2

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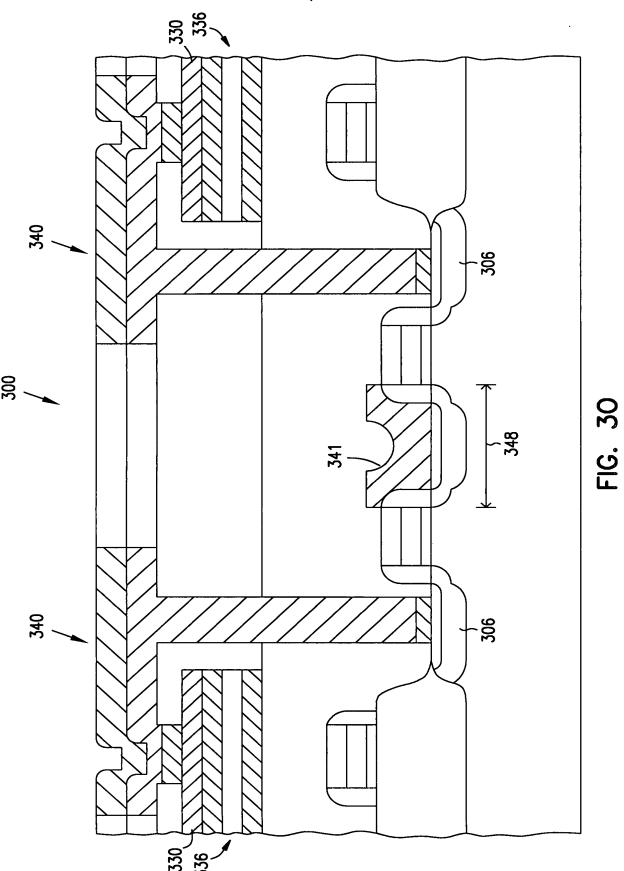


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INVENTOR'S NAME: VISHNU K. AGARWAL

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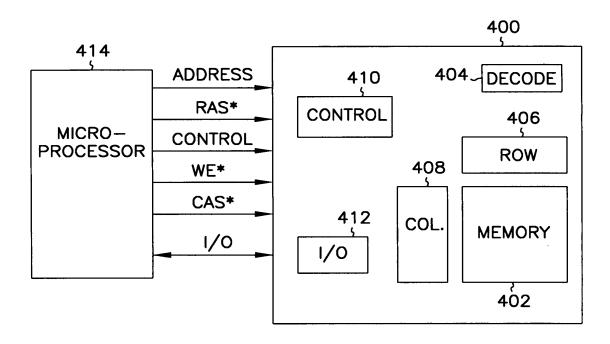
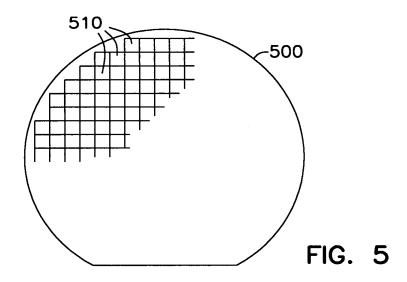


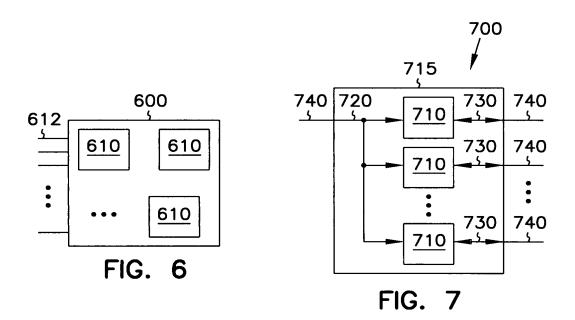
FIG. 4

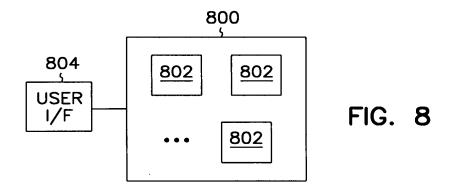
INVENTOR'S NAME: VISHNU K. AGARWAL

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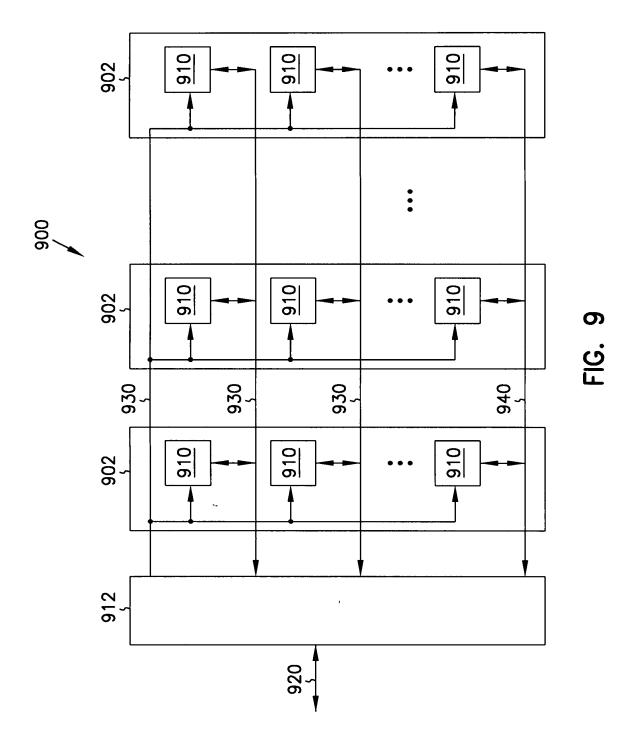




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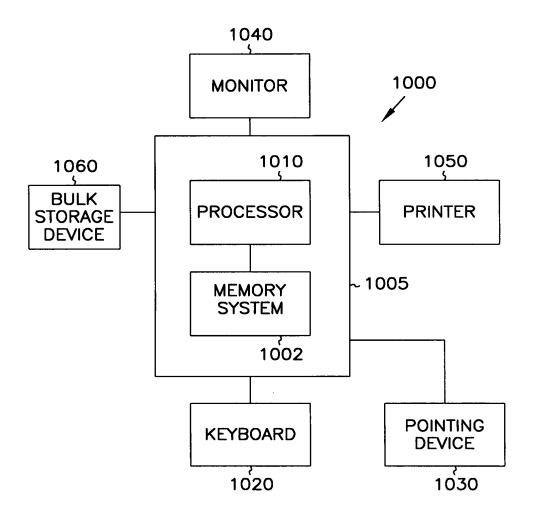


FIG. 10